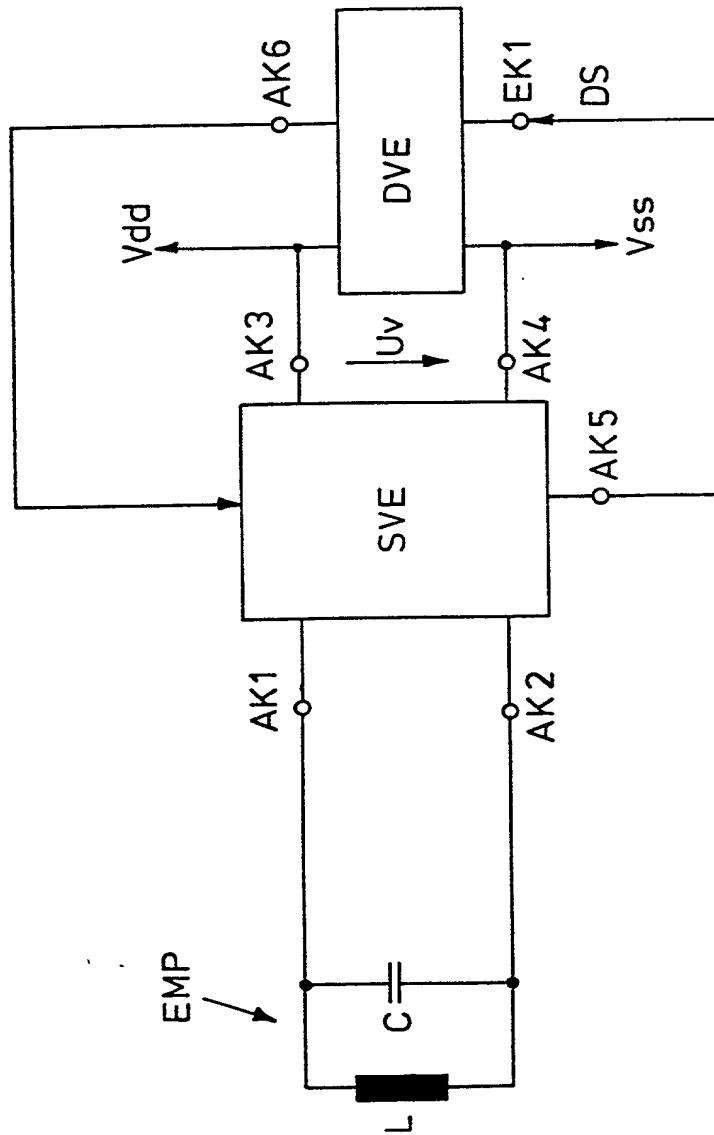


FIG 1



The diagram illustrates a complex signal processing circuit, likely a part of a larger system. Key components and connections include:

- Input Stage:** An EMP signal is applied to a network of inductors (L) and capacitors (C), connected to nodes AK1 and AK2. A diode is connected between AK1 and AK2.
- Control Signals:** Various control signals are present, including GL, AK9, AK10, AK11, AK12, AK51, AK52, AK61, AK62, DS1, and DS2.
- Logic Blocks:** The circuit contains several functional blocks: SR1, SR2, DVE, DEM1, and DEM2.
- Power and Biasing:** The circuit is powered by Vdd and Vss. A feedback loop with gain factor U_v is shown, connecting the output of the DVE block back to the input stage.
- Output Stage:** The final output is taken from node AK51, which is connected to DEM1 and DEM2.